

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20010056576".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:03
L2	305	725/68.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:03
L3	69	725/68,70,131,151.ccls. and multichannel	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:03
L4	177	725/68,70,131,151.ccls. and (multichannel (multi adj channel))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:03
L5	43	L4 and (pay paid subscrib\$5) and (free unpaid)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:03
L6	177	L4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:03
L7	16	L4 and descrambl\$4 and demultiplex\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L8	1	(multichannel (multi adj channel)) and (receiver decoder ird (set adj top) settop stt stb std) and (descrambl\$4) and (demultiplex\$4) and (Viaccess conax irdeto nagravisio) and ((inter adj integrated adj circuit) i2c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04

EAST Search History

L9	1	(multichannel (multi adj channel)) and (receiver decoder ird (set adj top) settop stt stb std) and (descrambl\$4) and (demultiplex\$4) and (Viaccess conax irdeto nagravision)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L10	3	(multichannel (multi adj channel)) and (receiver decoder ird (set adj top) settop stt stb std) and (descrambl\$4) and (demultiplex\$4) and ((inter adj integrated adj circuit) i2c)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L11	335	(multichannel (multi adj channel)) and (receiver decoder ird (set adj top) settop stt stb std) and (descrambl\$4) and (demultiplex\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L12	239	(multichannel (multi adj channel)) and (receiver decoder ird (set adj top) settop stt stb std) and (descrambl\$4) and (demultiplex\$4) and satellite	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L13	0	("2001/0056576").URPN.	USPAT	OR	ON	2006/07/19 16:04
L14	458	Viaccess Conax Cryptwork Irdeto Nagravision	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L15	385	Viaccess Conax Cryptwork Irdeto Nagravision and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L16	384	Viaccess Conax Cryptwork Irdeto Nagravision and "725".clas. and satellite and scrambl\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L17	382	Viaccess Conax Cryptwork Irdeto Nagravision and "725".clas. and satellite and scrambl\$4 and (I2C (inter adj integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04

EAST Search History

L18	382	Viaccess Conax Cryptwork Irdeto Nagravision and "725".clas. and satellite and scrambl\$4 and (I2C (inter adj integrated adj circuit)) and ((time adj lapse adj message) warning)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L19	382	Viaccess Conax Cryptwork Irdeto Nagravision and "725".clas. and satellite and scrambl\$4 and (I2C (inter adj integrated adj circuit)) and ((time adj lapse adj message) warning)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L20	1	(Viaccess Conax Cryptwork Irdeto Nagravision) and "725".clas. and satellite and scrambl\$4 and (I2C (inter adj integrated adj circuit)) and ((time adj lapse adj message) warning)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L21	1	(Viaccess Conax Cryptwork Irdeto Nagravision) and "725".clas. and satellite and scrambl\$4 and (I2C (inter adj integrated adj circuit))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L22	4	(Viaccess Conax Cryptwork Irdeto Nagravision) and "725".clas. and satellite and scrambl\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L23	9	(Viaccess Conax Cryptwork Irdeto Nagravision) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L24	458	(Viaccess Conax Cryptwork Irdeto Nagravision)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L25	0	(Viaccess Conax Cryptwork Irdeto Nagravision) and ("348".clas.)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04

EAST Search History

L26	131	(Viaccess Conax Cryptwork Irdeto Nagravision) and (receiver decoder (set adj top) settop stt stb std ird)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L27	2	"20010056576".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L28	75	(i2c (inter adj integrated adj circuit)) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L29	7296	(i2c (inter adj integrated adj circuit) ("I.sup.2" C)) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L30	3279	((i2c (inter adj integrated adj circuit) ("I.sup.2" C)) adj interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L31	310	((i2c (inter adj integrated adj circuit) ("I.sup.2" C)) adj interface) near5 (host CPU processor controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:04
L32	0	((i2c (inter adj integrated adj circuit) ("I.sup.2" C)) adj interface) near5 ((host CPU processor controller) and (transport adj stream adj interface))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:05
L33	4	((i2c (inter adj integrated adj circuit) ("I.sup.2" C)) adj interface) near5 (host CPU processor controller) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:05

EAST Search History

L34	0	((i2c (inter adj integrated adj circuit) ("I.sup.2")) adj interface) near5 (host CPU processor controller) and ""725"".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:05
L35	0	((i2c (inter adj integrated adj circuit) ("I.sup.2")) adj interface) near5 (host CPU processor controller) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:05
L36	1	"6279824".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/07/19 16:05
L37	0	((i2c (inter adj integrated adj circuit) ("I.sup.2\$")) adj interface) near5 (host CPU processor controller) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:08
L38	1	"725".clas. and (time adj lapse adj message)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:08
L39	412	satellite and (signals near5 descrambl\$4) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:08
L40	220	satellite and ((signals near5 descrambl\$4) same (message warning delay buffer))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:08
L41	80	satellite and ((signals near5 descrambl\$4) same (message warning delay buffer)) and "725".clas.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 16:08
L42	41	(smart adj card) and ("please wait")	US-PGPUB; USPAT; USOCR	OR	ON	2006/07/19 16:08